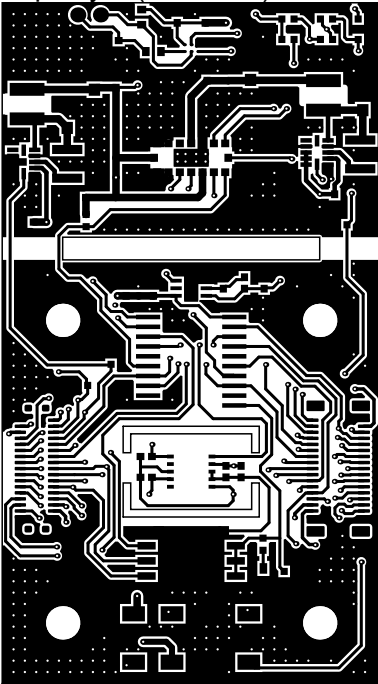


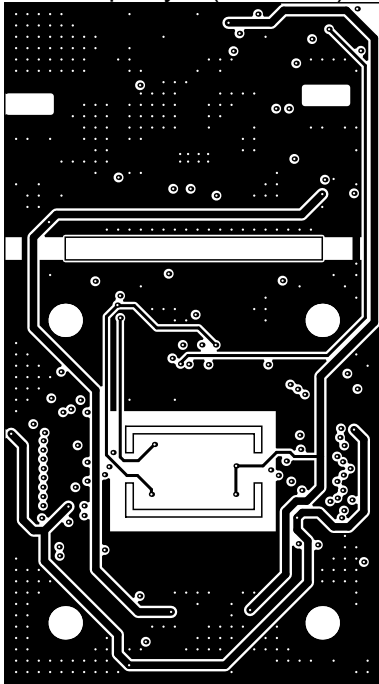
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REV STATUS OF SHEETS	REV										REVISIONS				
	SHEET										ZONE	REV	DESCRIPTION	DATE	APPROVED
Revision (Cont'd)															

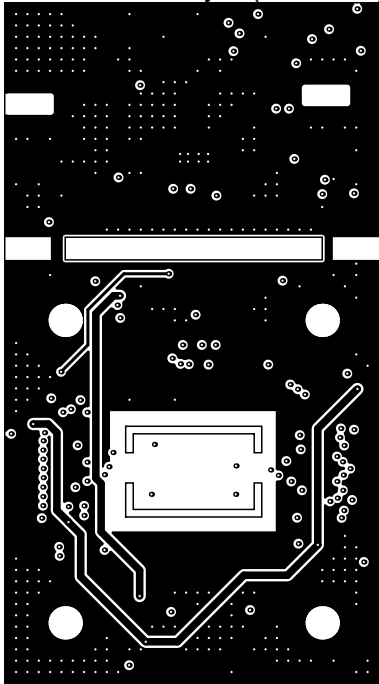
Top Layer (Scale 2:1)



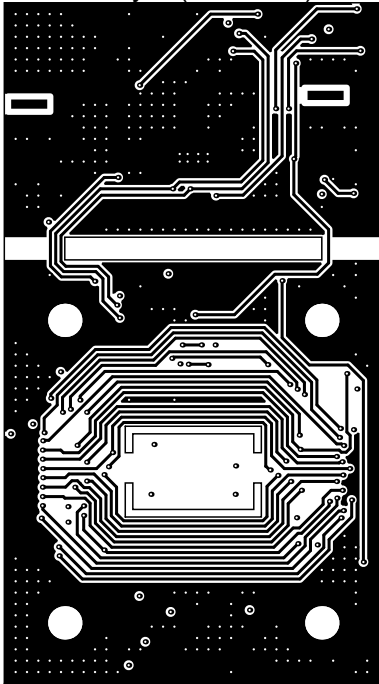
Inner Top Layer (Scale 2:1)



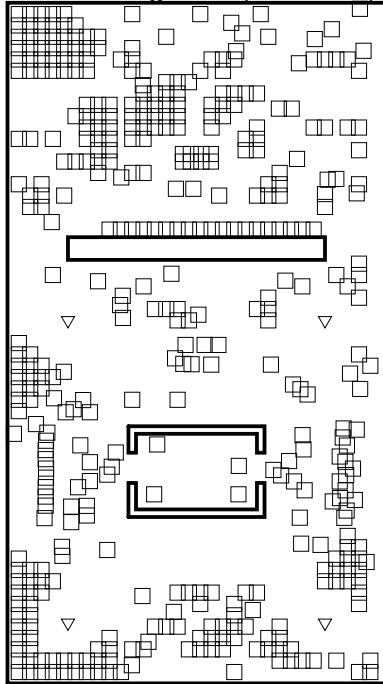
Inner Bottom Layer (Scale 2:1)



Bottom Layer (Scale 2:1)



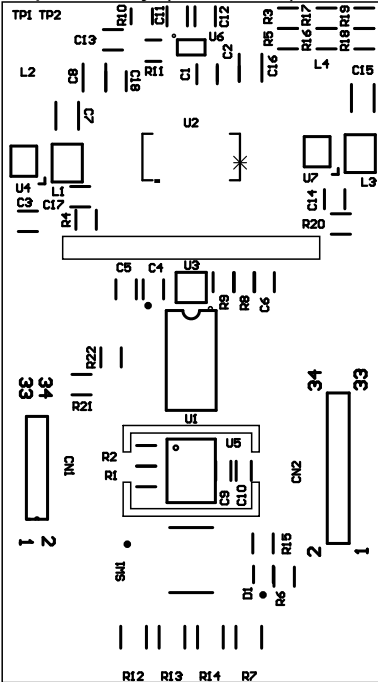
Drill Drawing View (Scale 2:1)



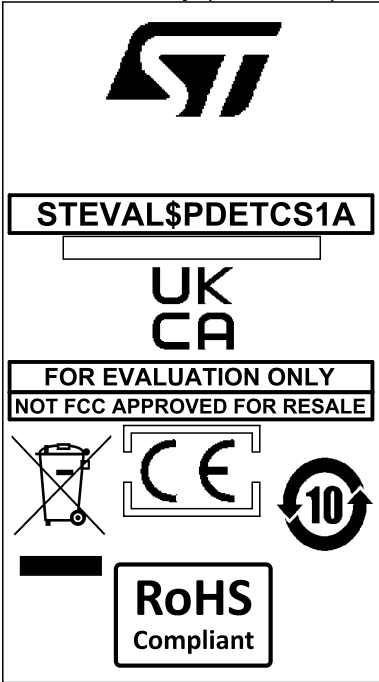
Drill Table

Symbol	Count	Hole Size	Plated	Hole Tolerance
□	419	0.15mm	Plated	
▽	4	2.20mm	Plated	
423 Total				

Top Overlay (Scale 2:1)



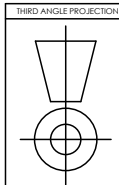
Bottom Overlay (Scale 2:1)



Layer Stack Legend

Material	Layer	Thickness	Dielectric Material	Type	Gerber
Surface Material	Top Overlay			Legend	GTO
CF-004	Top Solder	0.01mm	Solder Resist	Solder Mask	GTS
Prepreg	Top Layer	0.04mm	PP-006	Signal	GTL
Copper	Inner Top Layer	0.04mm		Signal	G1
		0.32mm	FR-4	Dielectric	
Copper	Inner Bottom Layer	0.04mm		Signal	G2
Prepreg	Bottom Layer	0.07mm	PP-006	Dielectric	
CF-004	Bottom Solder	0.04mm		Signal	GBL
Surface Material	Bottom Overlay	0.01mm	Solder Resist	Solder Mask	GBS
				Legend	GBO

Total thickness: 0.62mm



PART NO: =PCB_PART_NUMBER		APPROVALS		DATE	Altium™	=Address1	
ENGINEER: =PCB_ENGINEER		=PCB_ENGINEER				=Address2	
DESIGNER: =PCB_DESIGNER		=PCB_DESIGNER				=Address3	
CHECKER: =PCB_CHECKER		=PCB_CHECKER				=Address4	
BOM DOC: =DOC_NO_BOM		Reference Documents		DESIGN ITEM: .Item	DESIGN ITEM REVISION: .ItemRevision		
ASSY DOC: =DOC_NO_FAB_DWG		TITLE:		=PCB_TITLE_1			
SCH DOC: =DOC_NO_SCH_DWG		SIZE: A3		=PCB_TITLE_2			
PCB DOC: =PCB_DWG_NO		CAGE CODE: =CAGE_CO		REV:			
SCALE:		FILE NAME: STEVAL-PDETCS1.PCBDwf		SHEET: 1 OF 1			